

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Wolrich et al.                      Art Unit: 2187  
Serial No.: 10/780,330                      Examiner: Prasith Thammavong  
Filed : February 17, 2004  
Assignee : Intel Corporation  
Title : MEMORY MAPPING IN A PROCESSOR HAVING MULTIPLE  
PROGRAMMABLE UNITS (AS AMENDED)

**MAIL STOP AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicants call attention to the attached Information Disclosure Statement and documents listed on form PTO-1449.

The documents are in the English language; hence no concise explanation is necessary per Rule 98(a)(3).

Consideration of the foregoing and enclosures plus the return of a copy of the enclosed form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

Kindly accept this Information Disclosure Statement under Rule 97(c)(2). Please apply the rule 17(p) certification fee in the amount of \$180 to deposit account 06-1050.

Respectfully submitted,

Date: May 28, 2008

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